PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Fe application of: Rajski et al.

**Application No.** 10/781,031

Filed: February 17, 2004

Confirmation No. 2828

For: METHOD FOR SYNTHESIZING LINEAR

FINITE STATE MACHINES

Examiner: David H. Malzahn

Art Unit: 2193

Attorney Reference No. 1011-67627-01

MAIL STOP AMENDMENT COMMISSIONER FOR PATENTS P.O. BOX 1450 ALEXANDRIA, VA 22313-1450

#### **CERTIFICATE OF MAILING**

I hereby certify that this paper and the documents referred to as being attached or enclosed herewith are being deposited with the United States Postal Service as First Class Mail in an envelope addressed to: MAIL STOP AMENDMENT, COMMISSIONER FOR PATENTS, P.O. BOX 1450, ASEXANDRIA, VA 22313-1450 on the date shown below.

Attorney or Agent

Date Mailed June 29, 2005

# INFORMATION DISCLOSURE STATEMENT PURSUANT TO 37 C.F.R. § 1.97(c)

Listed on the accompanying form PTO-1449 and enclosed herewith is an English-language document and several foreign language documents. Applicants respectfully request that these documents be listed as references cited on the issued patent. This Information Disclosure Statement ("IDS") is being mailed before Applicants received a final action, a notice of allowance, or an action that otherwise closes prosecution in the referenced application.

Japanese Patent Publication No. 01-239486 recites an output response compressor.

Japanese Patent Publication No. 03-002579 recites a method and device for logic circuit test using compressed data. Japanese Patent Publication No. 05-215816 recites an information processing device. Japanese Patent Publication No. 11-030646 recites a semiconductor integrated circuit and test circuit to be comprised therein.

Copies of United States patents and United States published patent applications do not have to be provided to the Patent Office (37 C.F.R. 1.98(a)(2)(ii)). Copies of unpublished U.S.

07/07/2005 CNGUYEN 00000015 10781031

applications do not have to be provided, as long as the application is available on PAIR, as this requirement of 37 C.F.R. § 1.98(a)(2)(iii) has been waived by the United States Patent and Trademark Office pursuant to the Official Gazette Notice on October 19, 2004 (1287 OG 163). Applicants will provide copies of such patents or applications upon request.

Submitted herewith is a check for \$180.00 as required by 37 C.F.R. § 1.17(p) for filing this IDS in compliance with 1.97(c).

Please charge any additional fees which may be required in connection with filing this IDS, or credit any overpayment, to Deposit Account No. 02-4550.

The filing of this IDS shall not be construed to be an admission that the information cited in the statement is, or is considered to be, prior art or otherwise material to patentability as defined in 37 C.F.R. §1.56.

By

Respectfully submitted,

KLĄRQUIST SPARKMAN, LLP

Patrick M. Bible

Registration No. 44,423

One World Trade Center, Suite 1600

121 S.W. Salmon Street

Portland, Oregon 97204

Telephone: (503) 595-5300 Facsimile: (503) 228-9446

TPE

_	(O) ( ) (A)
	conns (2)
1	IUL 0 5 2003 (A)
١	
	INFORMATION DISCLOSURE STATEMENT
	BY APPLICANT

Attorney Docket Number	1011-67627-01
Application Number	10/781,031
Filing Date	February 17, 2004
First Named Inventor	Rajski
Art Unit	2193
Examiner Name	David H. Malzahn

### **U.S. PATENT DOCUMENTS**

Copies of U.S. Patent documents do not need to be provided, unless requested by the Patent and Trademark Office. For patents, provide the patent number and the issue date. For published U.S. applications, provide the publication number and the publication date. For unpublished pending patent applications, provide the application number and the filing date.

Examiner's Initials*	Cite No. (optional)	Number	Publication Date	Name of Applicant or Patentee

### FOREIGN PATENT DOCUMENTS

			Publication Date	Applicant or Patentee		
	Japan	01-239486	9.25.1989	Masaaki		
	Japan	03-002579	1.8.1991	Shuichi et al.		
	Japan	05-215816	8.27.1993	Yasushi		
	Japan	11-030646	2.2.1999	Hideki		
Cite No. (optional)	OTHER DOCUMENTS					
	Lew Yan Voon et al., "BIST Linear Generator based on Complemented Outputs," <i>IEEE VLSI Test Symposium</i> , pp. 137-142 (1992).					
		Japan Japan Japan Cite No. (optional) Lew Yan Voon	Japan       03-002579         Japan       05-215816         Japan       11-030646         Cite No. optional)       OT         Lew Yan Voon et al., "BIST Linear	Japan   03-002579   1.8.1991     Japan   05-215816   8.27.1993     Japan   11-030646   2.2.1999     Cite No. optional   OTHER DOCUMENTS     Lew Yan Voon et al., "BIST Linear Generator based on Company of the control of the contro		

EXAMINER	DATE
SIGNATURE:	CONSIDERED:

<sup>\*</sup> Examiner: Initial if reference considered, whether or not in conformance with MPEP 609. Draw line through cite if not in conformance and not considered. Include copy of this form with next communication to applicant.